

TECHNICAL DATA AND SPECIFICATIONS

DRAKE MPM860AG AGILE PROCESSOR MODULE

1002548

Input:

| | |
|------------------|---|
| Input Level: | - 39 dBm / + 10 dBmV, \pm 3 dB |
| Input Impedance: | 75 Ohm, 18 dB return loss |
| Input Frequency: | Channel 3 (61.25 MHz Visual Carrier) or Channel 4 (67.25 MHz Visual Carrier) Selectable via rear panel switch |

Output:

| | |
|--------------------------------|--|
| Frequency Range: | 54 to 864 MHz, Standard CATV Channels 2 to 135, Normal Operation. HRC, IRC Channels 1-135, selected via internal jumper. Broadcast Channels 2-69, selected via internal jumper |
| FCC Offsets: | Automatic, + 12.5 kHz or + 25 kHz channel dependent |
| Output Level: | + 45 dBmV |
| Output Impedance: | 75 Ohm, 10 dB return loss |
| Frequency Stability: | \pm 5 kHz |
| Spurious: | - 60 dB @ + 45 dBmV out |
| Broadband Noise: | -77 dBc @ greater than \pm 12 MHz offset at + 45 dBmV output, 4 MHz bandwidth |
| In Channel C/N: | 57 db, 4 MHz bandwidth |
| In Channel Frequency Response: | Within 2 dB |
| Output Level Note: | The output level of the MPM860AG is intended to be set at or very near + 45 dBmV. The front panel gain control adjusts the input level into the conversion circuitry and not the final output attenuation. For best noise and spurious performance please adjust the output level to +45 dBmV with the actual input source that will be used with the device. If more than 1 or 2 dB of variation is needed to properly equalize the system, appropriate attenuators should be placed on the output of the MPM860AG. |

General:

| | |
|------------------------|--|
| DC Power Input: | + 5 V or - 5 % at 400 mA +12 v or -5% at 200 mA |
| Operating Temperature: | 0° to 50° C, ambient |
| Size: | 1" W X 3.5" H X 7.5" D |
| Weight: | 12 ounces |

Specifications, price, and availability are subject to change without notice or obligation.